

FIG.1

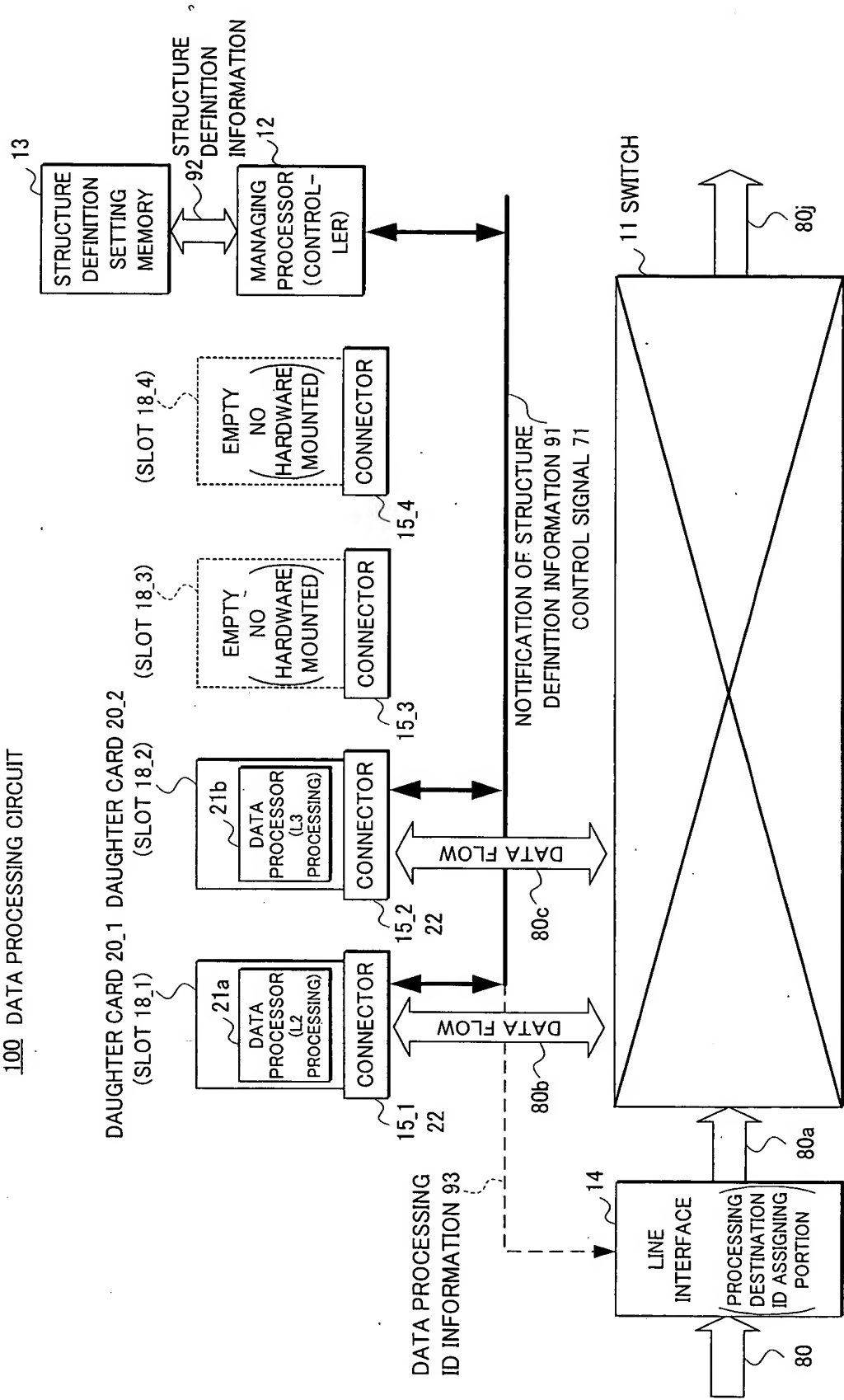


FIG.2

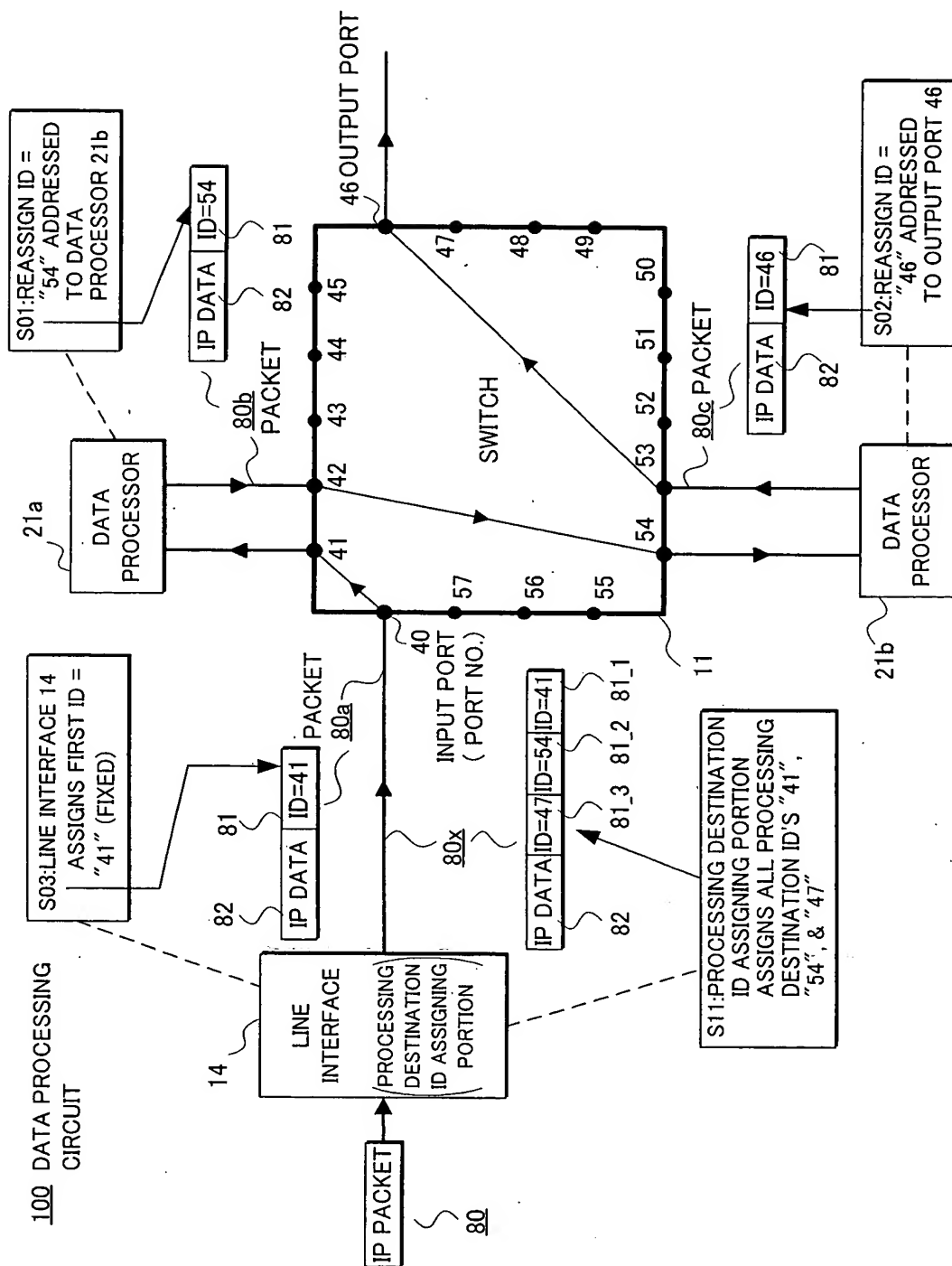


FIG.3

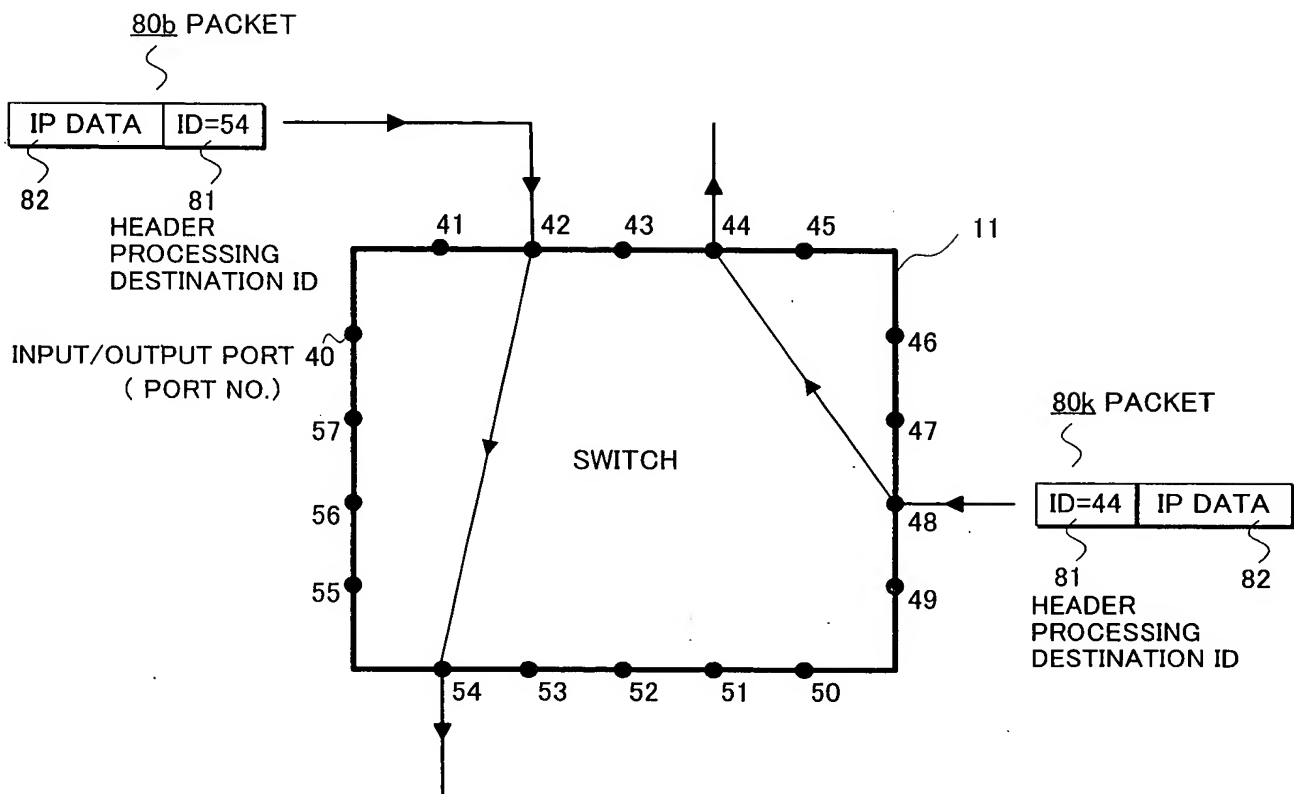


FIG.4A

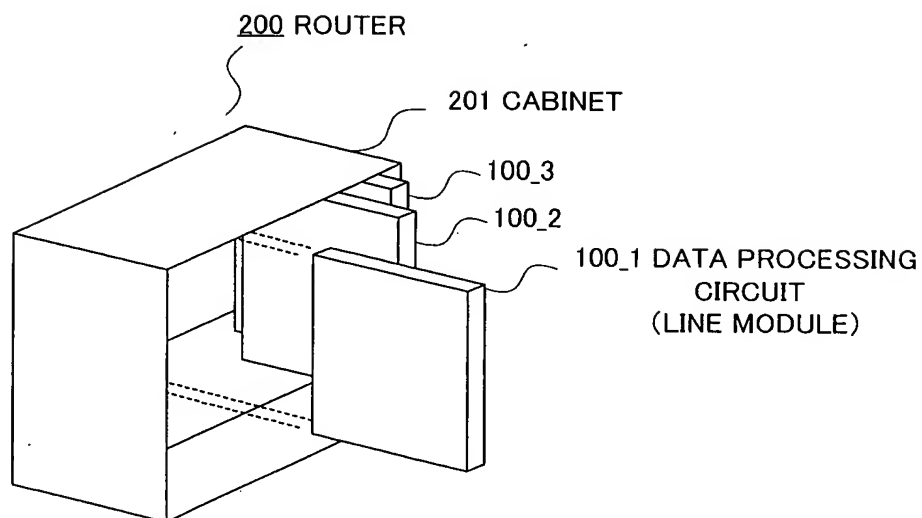


FIG.4B

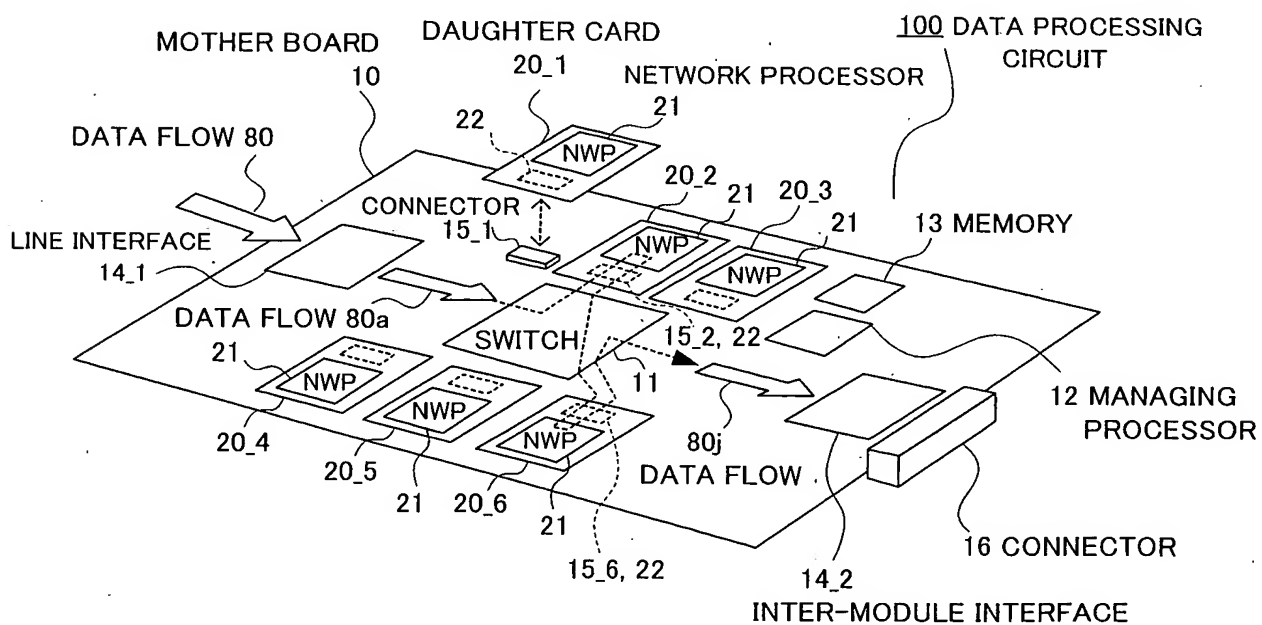


FIG.5

100 DATA PROCESSING CIRCUIT

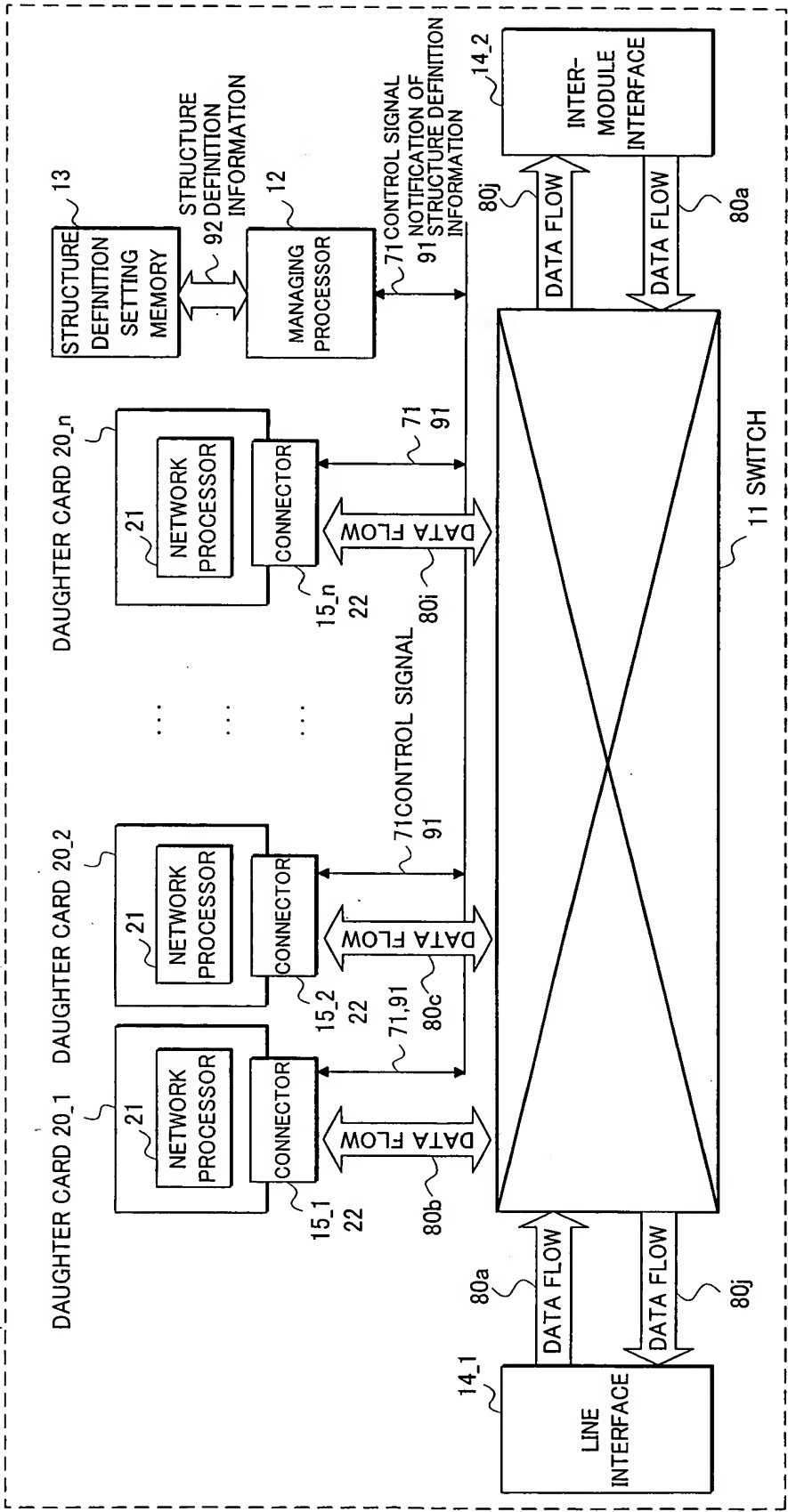


FIG.6A

100a DATA PROCESSING CIRCUIT

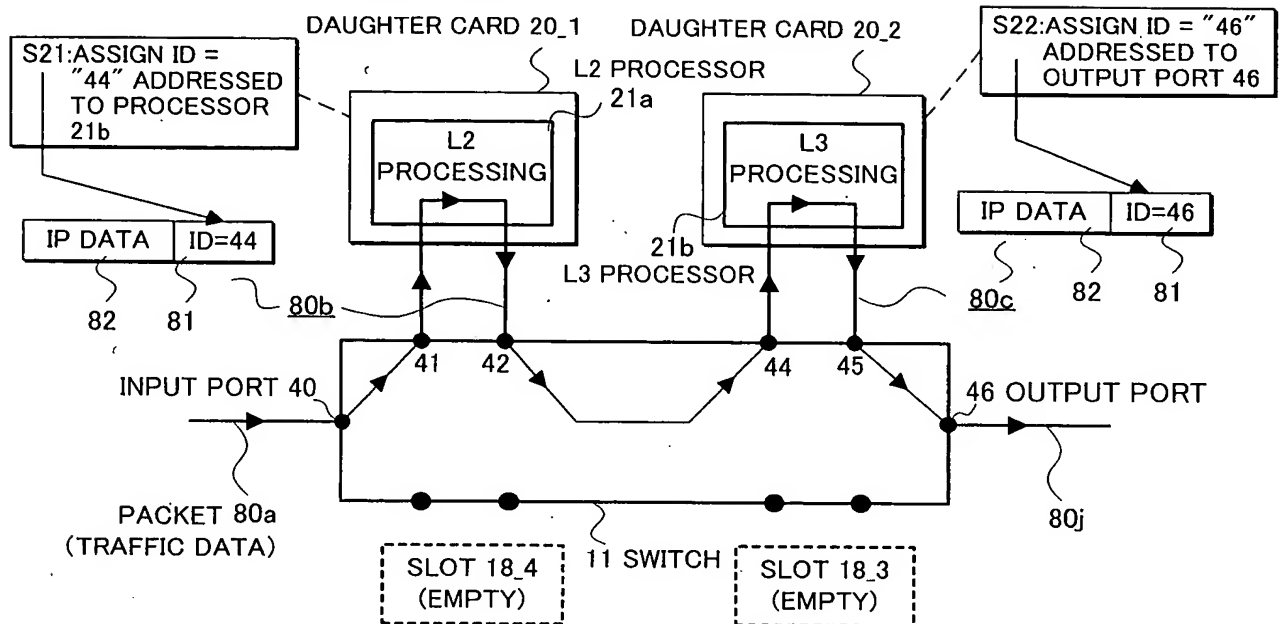


FIG. 6B

100b DATA PROCESSING CIRCUIT

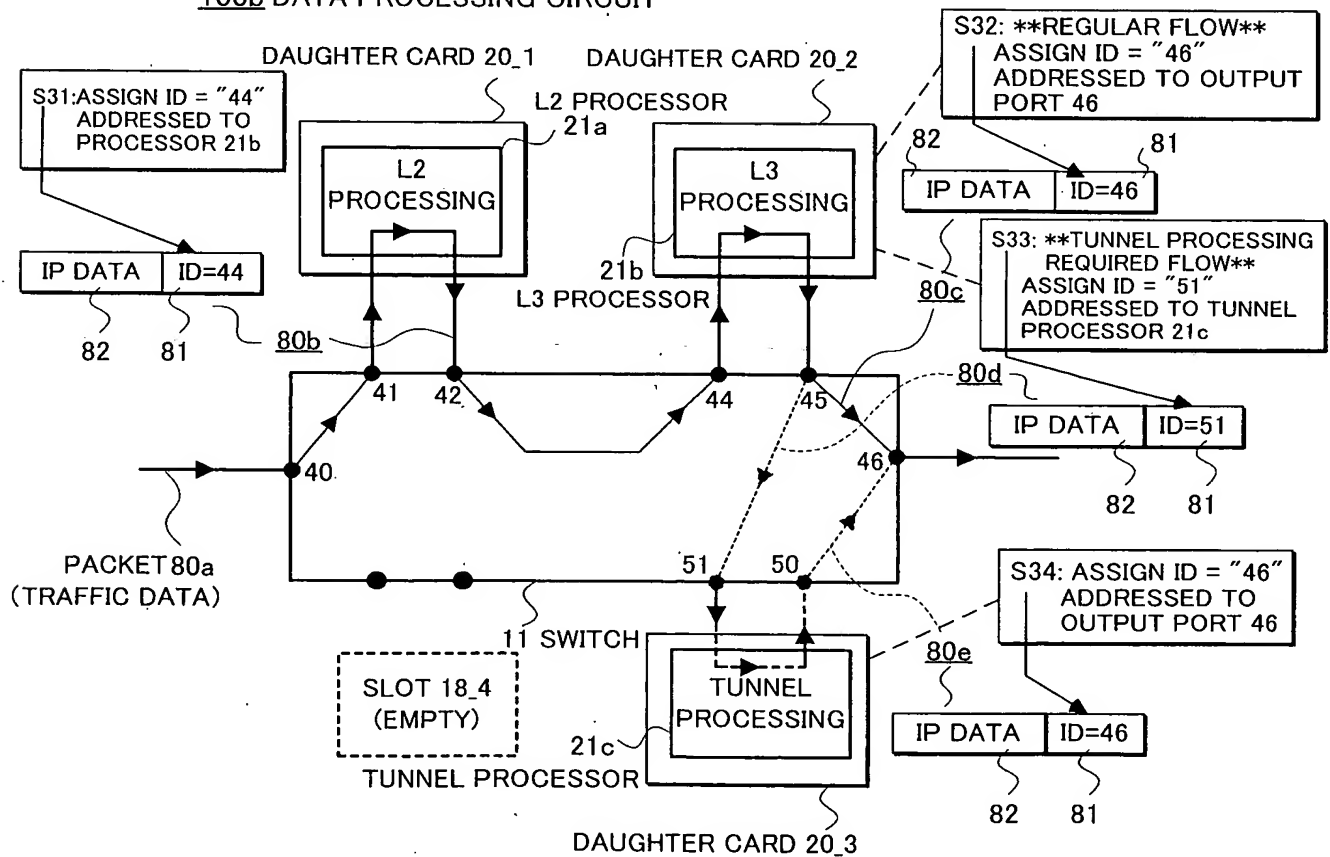


FIG. 7

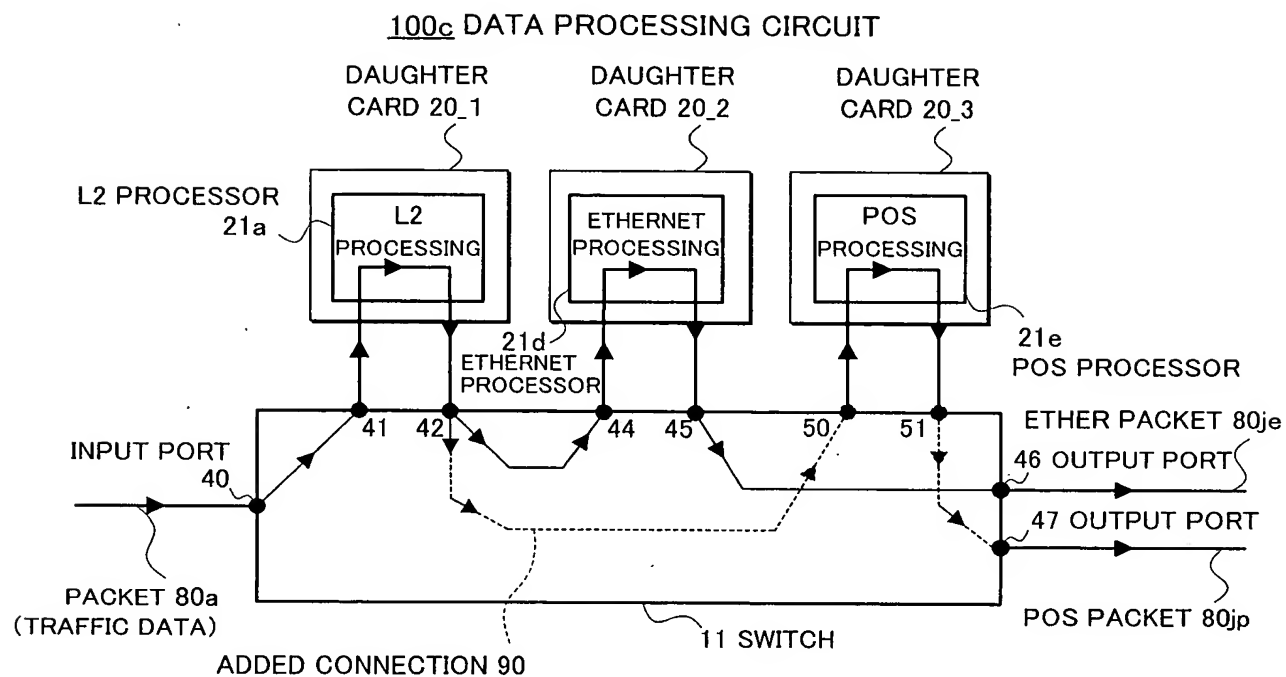


FIG.8A

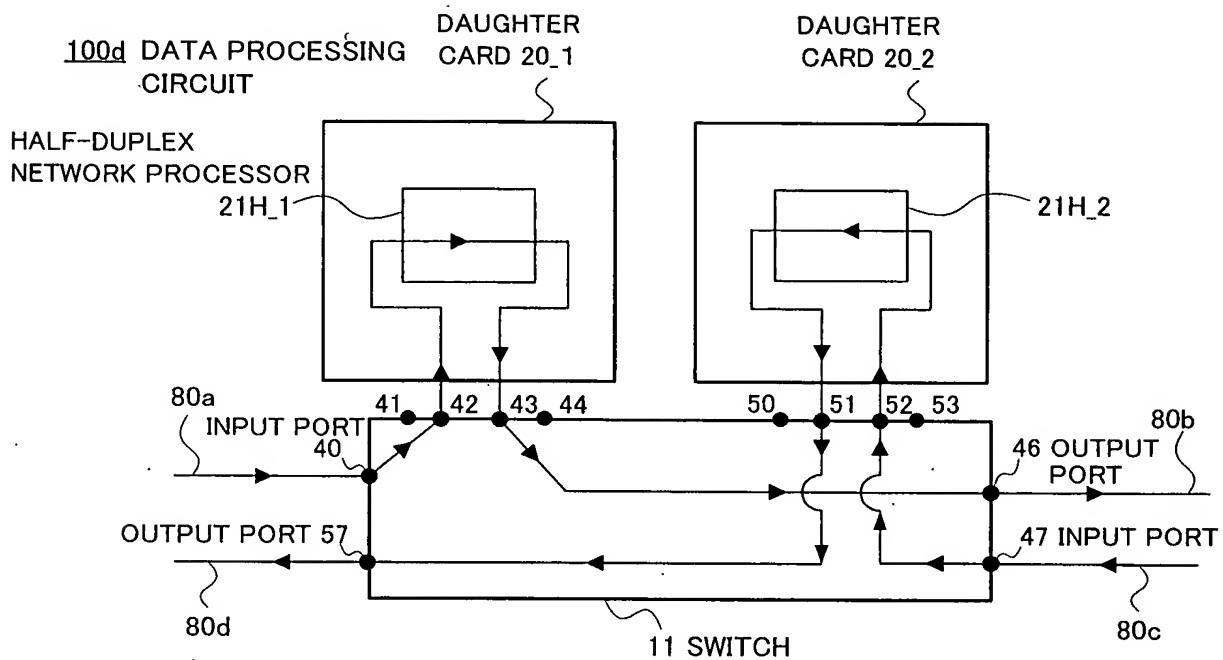


FIG.8B

